PATENT COOPERATION TREATY

PCT

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference P60206PCT		FOR FURTHER ACTION See Form PCT/IPEA/416				
International application No. PCT/EP2005/000698		International filing date (a 20.01.2005	ay/month/year)	Priority date (day/month/year) 29.01.2004		
	rnational Patent Classification (IPC) or 5K3/46, H05K3/18, H05K3/10	national classification and IPo				
	olicant OTECH DEUTSCHLAND GMB					
1.	Authority under Article 35 and tra	ansmitted to the applicant	according to Article 30	s International Preliminary Examining s.		
2.	2. This REPORT consists of a total of 5 sheets, including this cover sheet.					
3.	This report is also accompanied	by ANNEXES, comprising	g:			
	a. Sent to the applicant and to the International Bureau) a total of 1 sheets, as follows:					
	sheets of the description, claims and/or drawings which have been amended and are the basis of this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions).					
	beyond the disclosu Supplemental Box.	re in the international appl	ication as filed, as indi	iders contain an amendment that goes cated in item 4 of Box No. I and the		
	and or ticting and or t	Bureau only) a total of (in ables related thereto, in co be Listing (see Section 802	ombuter readable form	er of electronic carrier(s)) , containing a only, as indicated in the Supplemental Instructions).		
4.	This report contains indications	relating to the following ite	ems:			
		pinion				
	☐ Box No. II Priority	r				
	☐ Box No. III Non-establish	ment of opinion with rega	rd to novelty, inventive	step and industrial applicability		
1	☐ Box No. IV Lack of unity	of invention				
	M-Poy No. V. Reasoned sta	atement under Article 35(2 citations and explanations	 with regard to novelty supporting such states 	/, inventive step or industrial ment		
	Box No. VI Certain docui					
		ts in the international app				
	Box No. VIII Certain obser	rvations on the internation	ations on the international application			
Da	ate of submission of the demand		Date of completion of the	nis report		
21	1.06.2005		16.02.2006			
Na pre	ame and mailing address of the internateliminary examining authority:	tional	Authorized Officer	georgican Palacean, E.		
European Patent Office D-80298 Munich			Batev, P	· 110 (0)		
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10/587691 IAP12 Rec'd PCT/PTO 27 JUL 2006

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No. PCT/EP2005/000698

	Box No. I Basis of the repo	ort			
1.	With regard to the language , the filed, unless otherwise indicates	anguage, this report is based on the international application in the language in which it was ise indicated under this item.			
	This report is based on translations from the original language into the following language, which is the language of a translation furnished for the purposes of:				
	☐ publication of the inter	nder Rules 12.3 and 23.1(b)) national application (under Rule 12.4) ry examination (under Rules 55.2 and <i>l</i> or 55.3)			
2.	have been furnished to the red	of the international application, this report is based on (replacement sheets which ceiving Office in response to an invitation under Article 14 are referred to in this are not annexed to this report):			
	Description, Pages				
	1-37	as originally filed			
	Claims, Numbers				
	2-17	as originally filed			
	1	received on 21.06.2005 with letter of 17.06.2005			
	Drawings, Sheets				
	1/8-8/8	as originally filed			
	☐ a sequence listing and/or	any related table(s) - see Supplemental Box Relating to Sequence Listing			
3.	. The amendments have re	esulted in the cancellation of:			
	the description, pages				
	☐ the claims, Nos.☐ the drawings, sheets/figs				
	☐ the sequence listing <i>(specify)</i> :				
	☐ any table(s) related to	sequence listing (specify):			
4.	☐ This report has been established as if (some of) the amendments annexed to this report and listed below had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).				
	the description, pages				
	the claims, Nos.the drawings, sheets/f	igs			
	☐ the sequence listing (specify):			
	□ any table(s) related to	sequence listing (specify):			
		come or all of these sheets may be marked "superseded "			

International application No. PCT/EP2005/000698

Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. Statement

Novelty (N)

Yes: Claims

1-17

No: Claims

none

Inventive step (IS)

Yes: Claims No: Claims 1-17 none

Industrial applicability (IA)

Yes: Claims

1-17

No: Claims

none

2. Citations and explanations (Rule 70.7):

see separate sheet

Box No. VII Certain defects in the international application

The following defects in the form or contents of the international application have been noted:

see separate sheet

Box No. VIII Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made:

see separate sheet

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Re Item V

Reasoned statement with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

Reference is made to the following document:

- D1: US 2002/129972 A1 (KONRAD JOHN JOSEPH ET AL) 19 September 2002 (2002-09-19)
- 1. Document D1 discloses (paragraphs 18 39; figs. 1a 1d) a method of manufacturing a circuit carrier, the method comprising the following steps:
 - a) providing a substrate 1;
 - b) coating the substrate on one side thereof with a dielectric 2;
- c) structuring the dielectric for producing trenches and vias 3 therein using laser ablation:
 - d) depositing a primer layer 4 into the produced trenches and vias only;
- e) depositing a metal layer 5 onto the primer layer, with the trenches and vias being completely filled with metal for forming conductor structures therein.

Thus, the subject matter of claim 1 differs from this known method at least in that

- i) in step a) a printed circuit board is provided instead of a substrate; and
- ii) the trenches produced in step c) do not extend completely through the dielectric.

Consequently, the subject matter of claim 1 is new (Article 33(2) PCT).

2. Even if the skilled person interchanged the substrate in step a) of the known method with a circuit board, e.g. a single sided board, and used the side free of conductors as an equivalent to using a substrate, he would not arrive at the subject matter of claim 1 as he has no incitement to provide trenches which do not extend completely through the dielectric.

None of the other cited documents, which reflect the technological background, discloses or gives an incitement to the specific solution proposed in claim 1.

In view of the available prior art, the subject matter of claim 1 appears, therefore, to involve an inventive step (Article 33(3) PCT).

3. Claims 2 - 16 which define preferred embodiments of the invention are dependent on

claim 1 and as such also meet the requirements of the PCT in respect of novelty and inventive step.

Independent claim 17 is directed to an use of the method defined in claim 1. Its subject matter is therefore also new and inventive.

Re Item VII

Certain defects in the international application

1. Contrary to the requirements of Rule 5.1(a)(ii) PCT, the relevant background art disclosed in the document D1 is not mentioned in the description, nor is this document identified therein (see also the Guidelines, paragraph 4.05).

Re Item VIII

Certain observations on the international application

1. The last paragraph on page 36 of the description implies that the subject matter for which protection is sought may be different to that defined by the claims. This results in lack of clarity of the claims (Article 6 PCT) when the description is used to interpret them (see the Guidelines, paragraph 5.30).

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PCT/EP2005/000698 Atotech Deutschland GmbH 17.06.2005

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Claims:

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- 1. A method of manufacturing a circuit carrier, said method comprising the following method steps:
 - a) Providing a printed circuit board;
 - b) Coating the circuit board on at least one side thereof with a dielectric;
 - c) Structuring the dielectric for producing trenches and vias therein using laser ablation, the trenches not extending completely through the dielectric;
 - Depositing a primer layer onto the entire surface of the dielectric or depositing the primer layer into the produced trenches and vias only;
 - e) Depositing a metal layer onto the primer layer, with the trenches and vias being completely filled with metal for forming conductor structures therein; and
 - f) Removing the metal layer and the primer layer, except for in the trenches and vias, to expose the dielectric if the primer layer has been deposited onto the entire surface in method step d).

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